4 Channel ADC, Cal Ref, SPI Clk

Global Net Ports

Development of 4 Channel ADC, Cal Ref, SPI Clk

Power Down Isolation
AD7716 config from SPI
Readout clk isolation from altera
for lower power operation
TESTPOINT MULTIPLEXOR
Dual Phase Lock Loop

Extra 4050's Cannot Be Used
Anti- Alias Filter
(Page 288 Filter Design Paper)

Filter Notes

All Resistors For Each Filter Module Mounted On 20 Pin Carrier

All Resistors & Capacitors Are 1%

Capacitor Values Are The Same For All Filter Corner Frequencies

MAX479's Are Pkg'ed By Stages

M1

FM100

Filter Module Link

Title | Anti- Alias Filter
--- | ---
Size | A
Number | 608-262-9698
Rev | 1.0
Date | September 1994
Drawn by | Jhg
Filename | MFILT.S01
Sheet | 1 of 1
Anti- Alias Filter
(Page 288 Filter Design Paper)

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1 - Complete Pkg
UF1:A,B,C,D

1 - Complete Pkg
UF1:A,B,C,D

M2
FM100
Filter Module Link

Title Anti- Alias Filter

Size A

Number 608-262-9698

Rev 1.0

Date September 1994

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Filename MFILT.S01

Sheet 1 of 1
Anti- Alias Filter
(Page 288 Filter Design Paper)

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1 - Complete Pkg
UFF1:A,B,C,D

M3
FM100
Filter Module Link

Title
Anti- Alias Filter

Size
A

Number
608-262-9698

Rev
1.0

Date
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Drawn by
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Filename
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Sheet 1 of 1
Anti- Alias Filter
(Page 288 Filter Design Paper)

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UF1:A,B,C,D

1 - Complete Pkg
UFF1:A,B,C,D

M4
FM100
Filter Module Link